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Levy, Sanford M.;
Electronic Computers, IEEE Transactions on
Volume EC-12, Issue 5, Oct. 1963 Page(s):573 - 573
Digital Object Identifier 10.1109/PGEC.1963.263663

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Pitchumani, V.; Stabler, E.P.;
Computers, IEEE Transactions on
Volume C-32, Issue 12, Dec. 1983 Page(s):1073 - 1080
Digital Object Identifier 10.1109/TC.1983.1676167

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☐ 4. **Analysis of real-time rule-based systems with behavioral constra assertions specified in Estella**

Cheng, A.M.K.; Browne, J.C.; Mok, A.K.; Rwo-Hsi Wang;
Software Engineering, IEEE Transactions on
Volume 19, Issue 9, Sept. 1993 Page(s):863 - 885
Digital Object Identifier 10.1109/32.241770

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5. Order structure of symbolic assertion objects

Brito, P.;
Knowledge and Data Engineering, IEEE Transactions on
Volume 6, Issue 5, Oct. 1994 Page(s):830 - 835
Digital Object Identifier 10.1109/69.317710

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6. Linear complexity assertions for sorting

Saxena, N.R.; McCluskey, E.J.;
Software Engineering, IEEE Transactions on
Volume 20, Issue 6, June 1994 Page(s):424 - 431
Digital Object Identifier 10.1109/32.295891

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7. A practical approach to programming with assertions

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Software Engineering, IEEE Transactions on
Volume 21, Issue 1, Jan. 1995 Page(s):19 - 31
Digital Object Identifier 10.1109/32.341844

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8. Floating point fault tolerance with backward error assertions

Boley, D.; Golub, G.H.; Makar, S.; Saxena, N.; McCluskey, E.J.;
Computers, IEEE Transactions on
Volume 44, Issue 2, Feb. 1995 Page(s):302 - 311
Digital Object Identifier 10.1109/12.364541

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9. Extending backward error assertions to tolerance of large errors floating point computations

Fitzpatrick, P.;
Computers, IEEE Transactions on
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Digital Object Identifier 10.1109/12.588072

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Voas, J.;
Software, IEEE
Volume 14, Issue 2, Mar/Apr 1997 Page(s):118 - 119, 122
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11. Using word-level ATPG and modular arithmetic constraint-solving techniques for assertion property checking

Chung-Yang Huan; Kwang-Ting Cheng;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on
Volume 20, Issue 3, March 2001 Page(s):381 - 391

Digital Object Identifier 10.1109/43.913756

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12. Foundations of the trace assertion method of module interface specification

Janicki, R.; Sekerinski, E.;
Software Engineering, IEEE Transactions on
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Digital Object Identifier 10.1109/32.935852

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13. Assertions: a personal perspective

Hoare, C.A.R.;
Annals of the History of Computing, IEEE
Volume 25, Issue 2, April-June 2003 Page(s):14 - 25
Digital Object Identifier 10.1109/MAHC.2003.1203056

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Volume SE-5, Issue 4, July 1979 Page(s):359 - 367

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Software Engineering, IEEE Transactions on
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16. A Note on Synthesis of Inductive Assertions

Basu, S.K.;
Software Engineering, IEEE Transactions on
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King, J.C.;
Software Engineering, IEEE Transactions on
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Fairley, R.E.;
Software Engineering, IEEE Transactions on
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Pearl, J.;
Information Theory, IEEE Transactions on
Volume 23, Issue 4, Jul 1977 Page(s):459 - 465

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20. Design and implementation of assertions for the Common Langua Infrastructure

Tran, N.; Mingins, C.; Abramson, D.;
Software, IEE Proceedings -
Volume 150, Issue 5, 27 Oct. 2003 Page(s):329 - 336
Digital Object Identifier 10.1049/ip-sen:20030988

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21. The Assertion of Possibilities in `Indian-Jaina' Logic

Gupta, Madan M.;
American Control Conference, 1983
22-24 June 1983 Page(s):1240 - 1240

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22. A three-tier assertion technique for SPICE verification of transisto timing analysis

Savithri, S.; Blaauw, D.G.;
VLSI Design, 1999. Proceedings. Twelfth International Conference On
7-10 Jan. 1999 Page(s):175 - 180
Digital Object Identifier 10.1109/ICVD.1999.745145

AbstractPlus | Full Text: PDF(56 KB)   IEEE CNF
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23. Efficient generation of monitor circuits for GSTE assertion graphs

Hu, A.J.; Cases, J.; Jin Yang;
Computer Aided Design, 2003. ICCAD-2003. International Conference on
9-13 Nov. 2003 Page(s):154 - 159
Digital Object Identifier 10.1109/ICCAD.2003.1257620

AbstractPlus | Full Text: PDF(554 KB)   IEEE CNF
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24. Building software recovery assertions from a fault injection-base propagation analysis

Voas, J.;
Computer Software and Applications Conference, 1997. COMPSAC '97. Proceedings., The Tw Annual International
13-15 Aug. 1997 Page(s):505 - 510
Digital Object Identifier 10.1109/CMPSAC.1997.625059

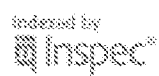AbstractPlus | Full Text: PDF(516 KB)   IEEE CNF
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25. Executable assertions and timed traces for on-line software error detection

Rabejac, C.; Blanquart, J.-P.; Queille, J.-P.;
Fault Tolerant Computing, 1996., Proceedings of Annual Symposium on
25-27 June 1996 Page(s):138 - 147

Digital Object Identifier 10.1109/FTCS.1996.534602

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